

OCT 04 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/635,715
 Confirmation No. 2007
 Filing Date August 5, 2003
 Inventor W. G. Scott
 Assignee Micron Technology, Inc.
 Group Art Unit 2812
 Examiner Richard A. Booth
 Attorney's Docket No. MI22-2375
 Customer No. 021567
 Title: Methods of Forming Transistor Gates

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

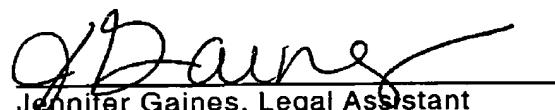
CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at 571-273-8300 on the date shown below:

1. Certificate of Facsimile Transmission
2. Response to August 15, 2005 FINAL Office Action

Dated: October 4, 2005

By:


 Jennifer Gaines, Legal Assistant
 Telephone No. (509) 624-4276
 Facsimile No. (509) 838-3424

FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: October 4, 2005

By:


 David G. Latwesen, Ph.D.
 Reg. No. 38,533
NUMBER OF PAGES IN FACSIMILE: 6

OCT 04 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/635,715
 Confirmation No. 2007
 Filing Date August 5, 2003
 Inventor W.G. Scott
 Assignee Micron Technology, Inc.
 Group Art Unit 2812
 Examiner Richard A. Booth
 Attorney's Docket No. MI22-2375
 Customer No. 021567
 Title: Methods of Forming Transistor Gates

RESPONSE TO AUGUST 15, 2005 FINAL OFFICE ACTION

Response to Final Office Action

To: Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
 Wells St. John P.S.
 601 W. First Avenue, Suite 1300
 Spokane, WA 99201-3828

AMENDMENTS